

## CONFIGURABLE MEMORY SYSTEM FOR EMBEDDED PROCESSORS

Klaus J. Oberlaender

Ralph Haines

## ABSTRACT OF THE DISCLOSURE

An embedded processor architecture includes a processing core with configurable memory system. Memory components can be inserted, omitted and resized in different configuration of the memory system without causing irregular features that may cause wasted silicon area. Furthermore, all the various configurations of the memory system are designed to interface with the processing core so that the processing core can be reused without change.